

1 / 14

FIG. 1

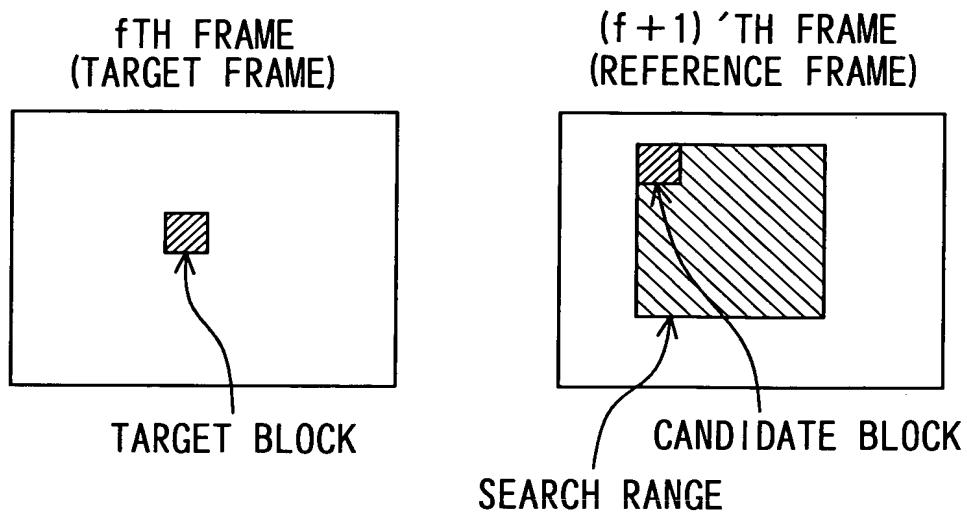
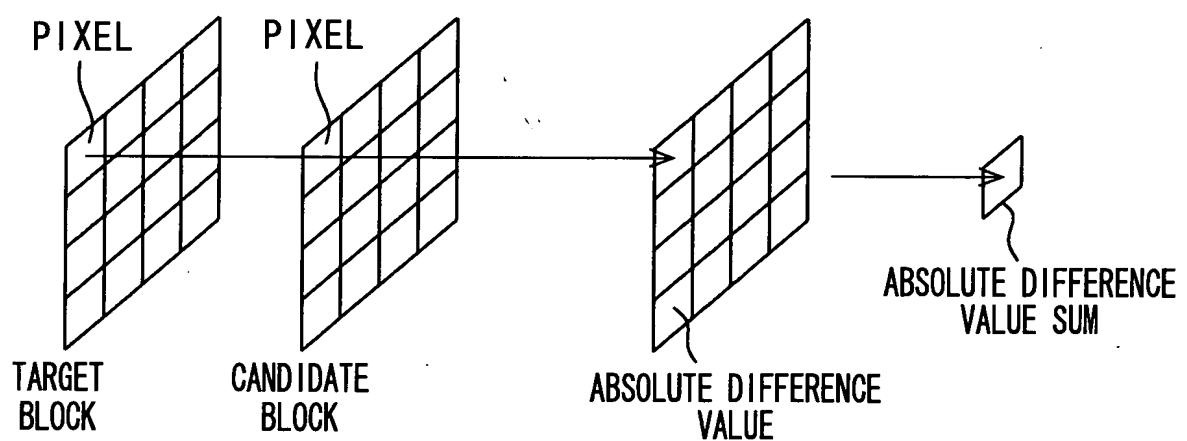


FIG. 2



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FIG. 3

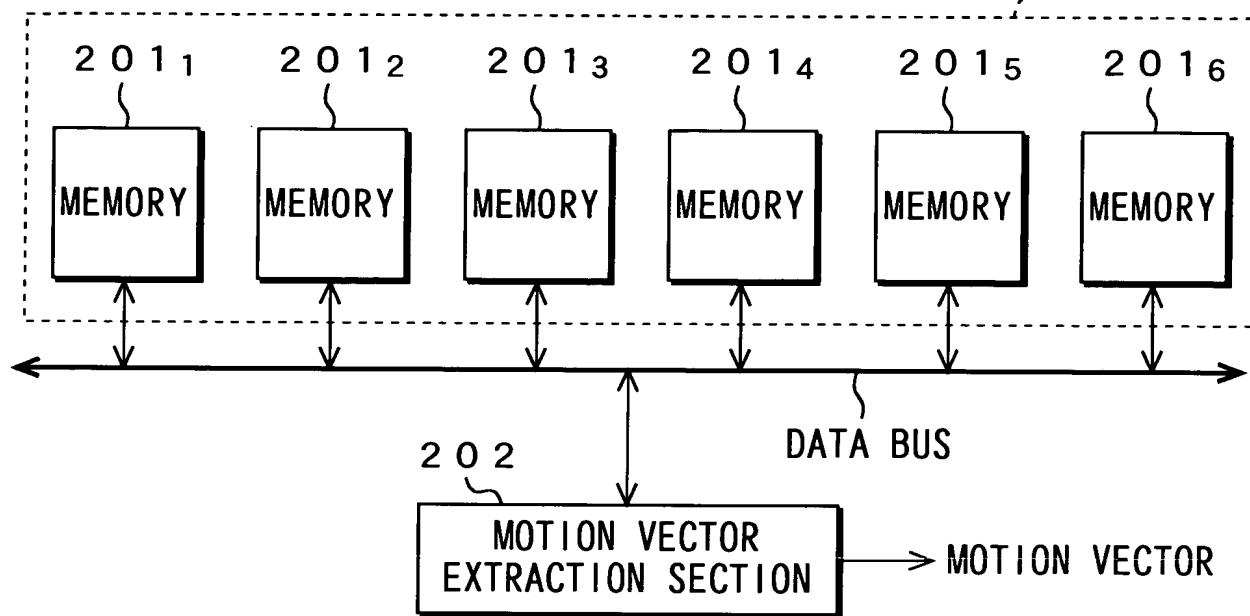


FIG. 4

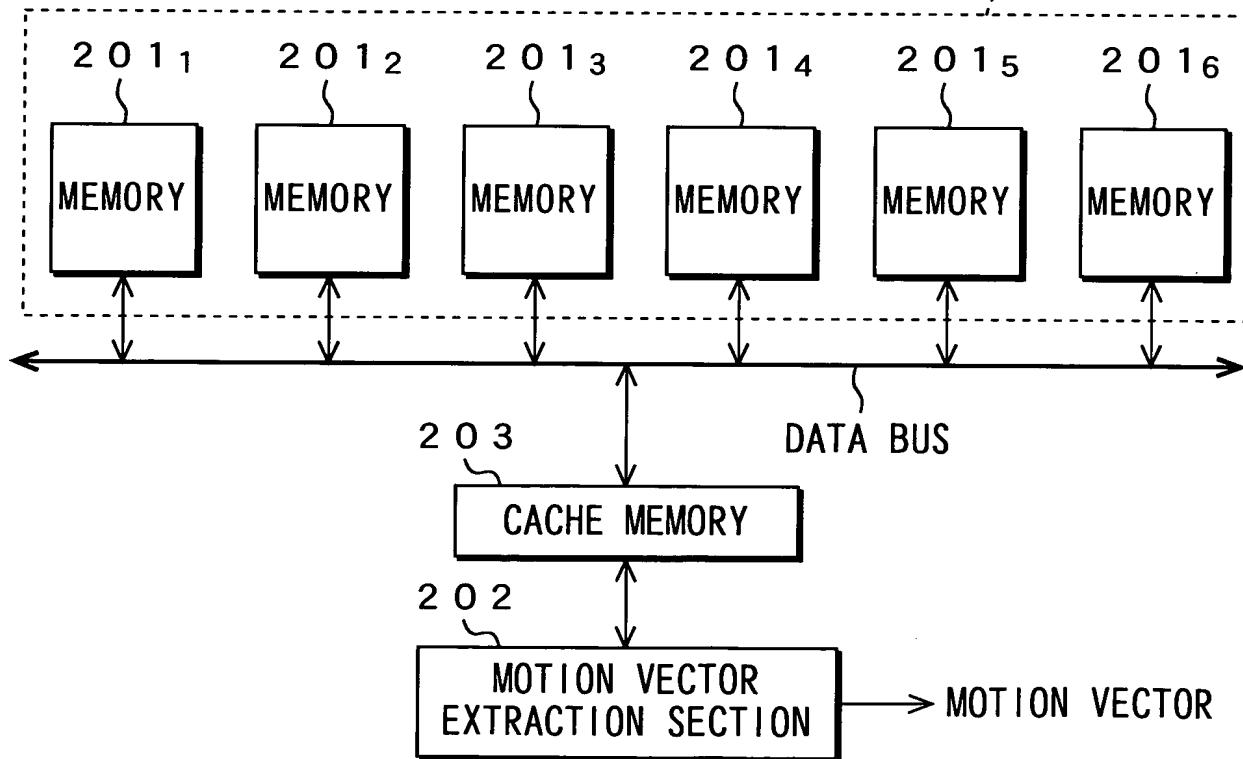


FIG. 5

1000

1024

1023

1022

1021

1020

1029

1028

1027

1026

1025

10210

10211

10212

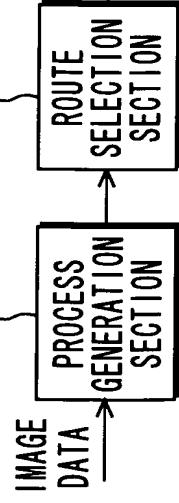
1041

1042

1043

101

1041

IMAGE
DATA

102a

120

110

PE

MEMORY

120

FIG. 6

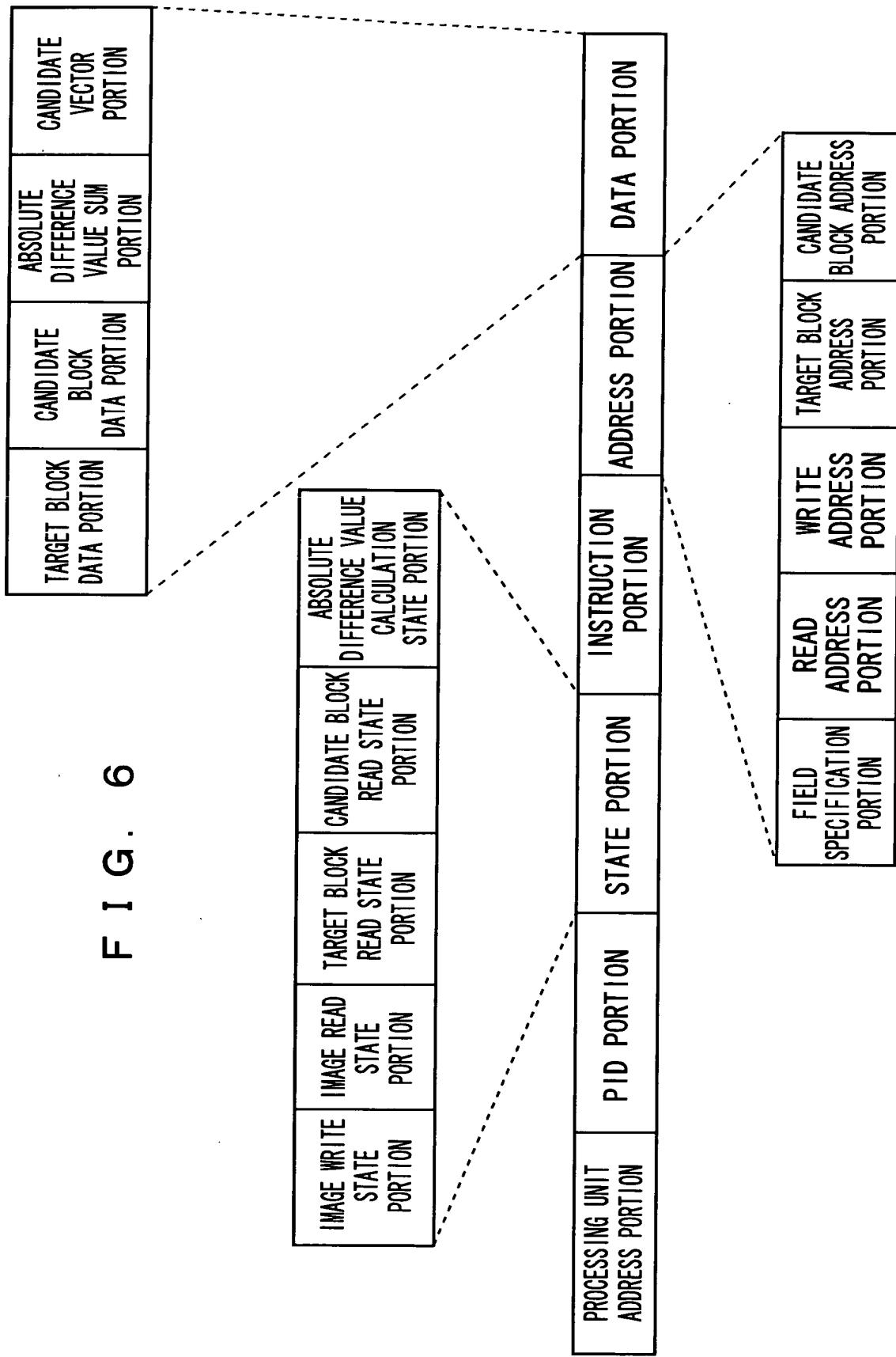
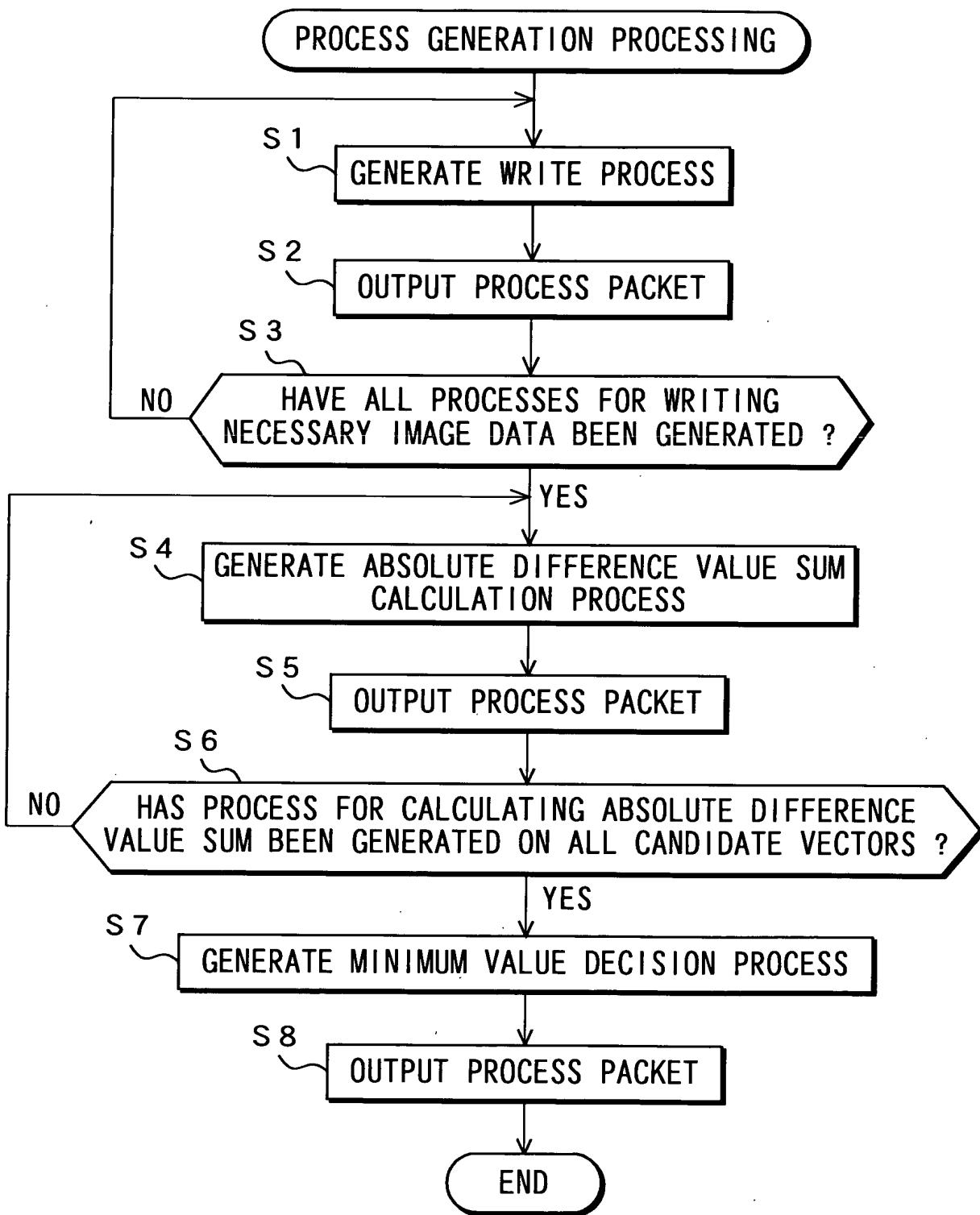


FIG. 7



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FIG. 8

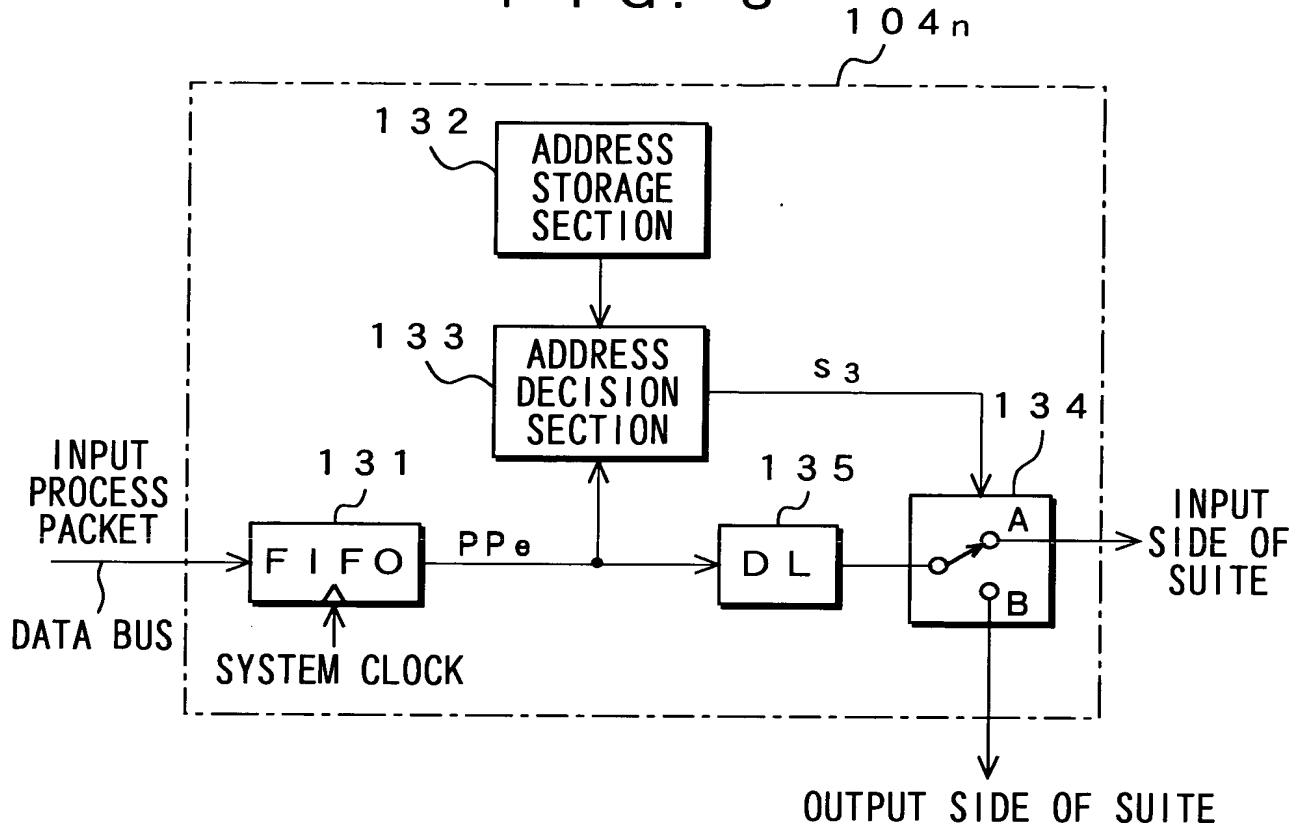


FIG. 10

DECISION SIGNAL s_1	REQUEST SIGNAL s_2	TRANSFER SWITCH 116
0	0	B
0	1	A
1	0	OFF
1	1	A

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102n

DIVIDED INTO 5 FIELDS

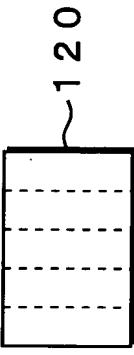


FIG. 9

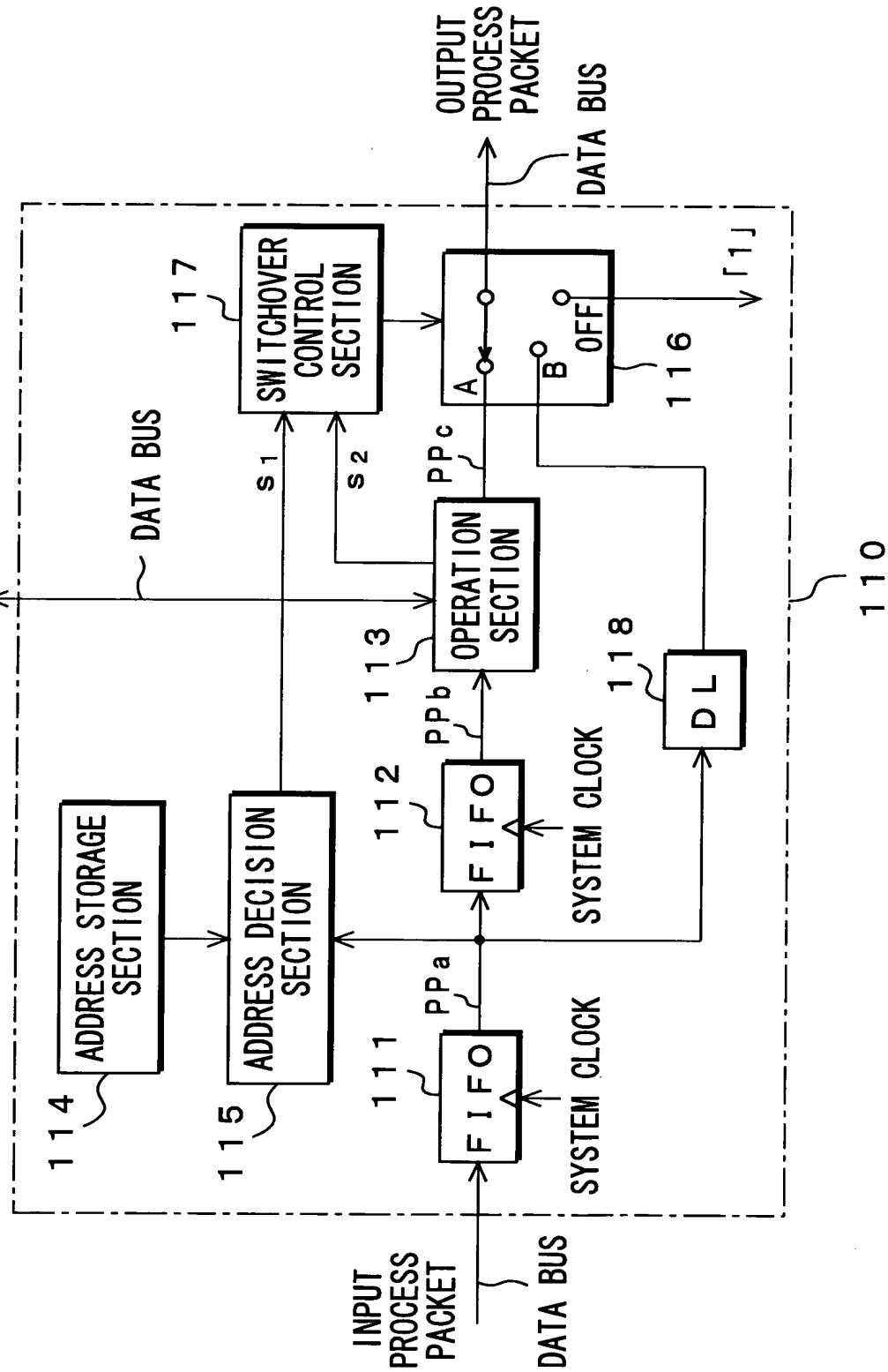


FIG. 11

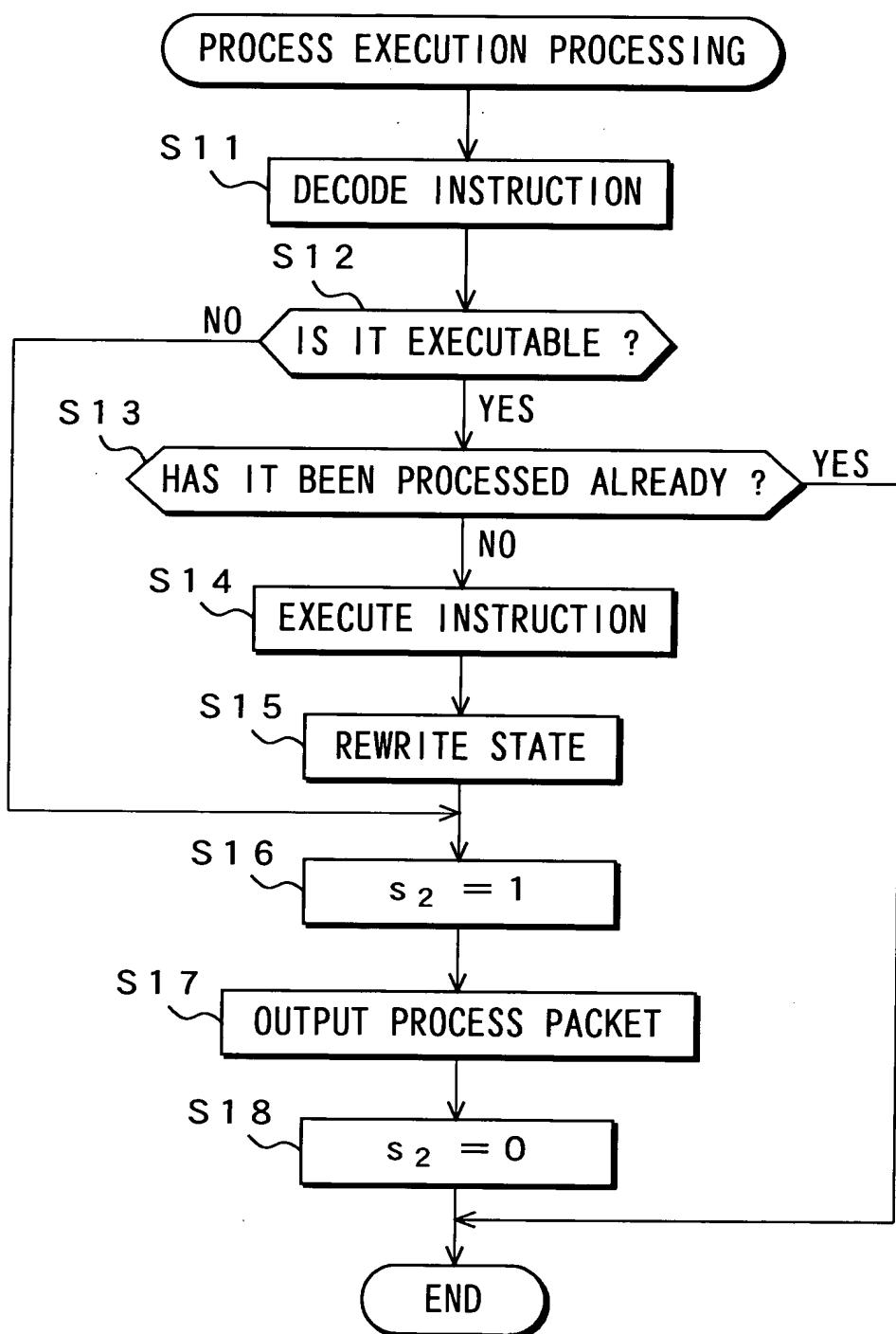
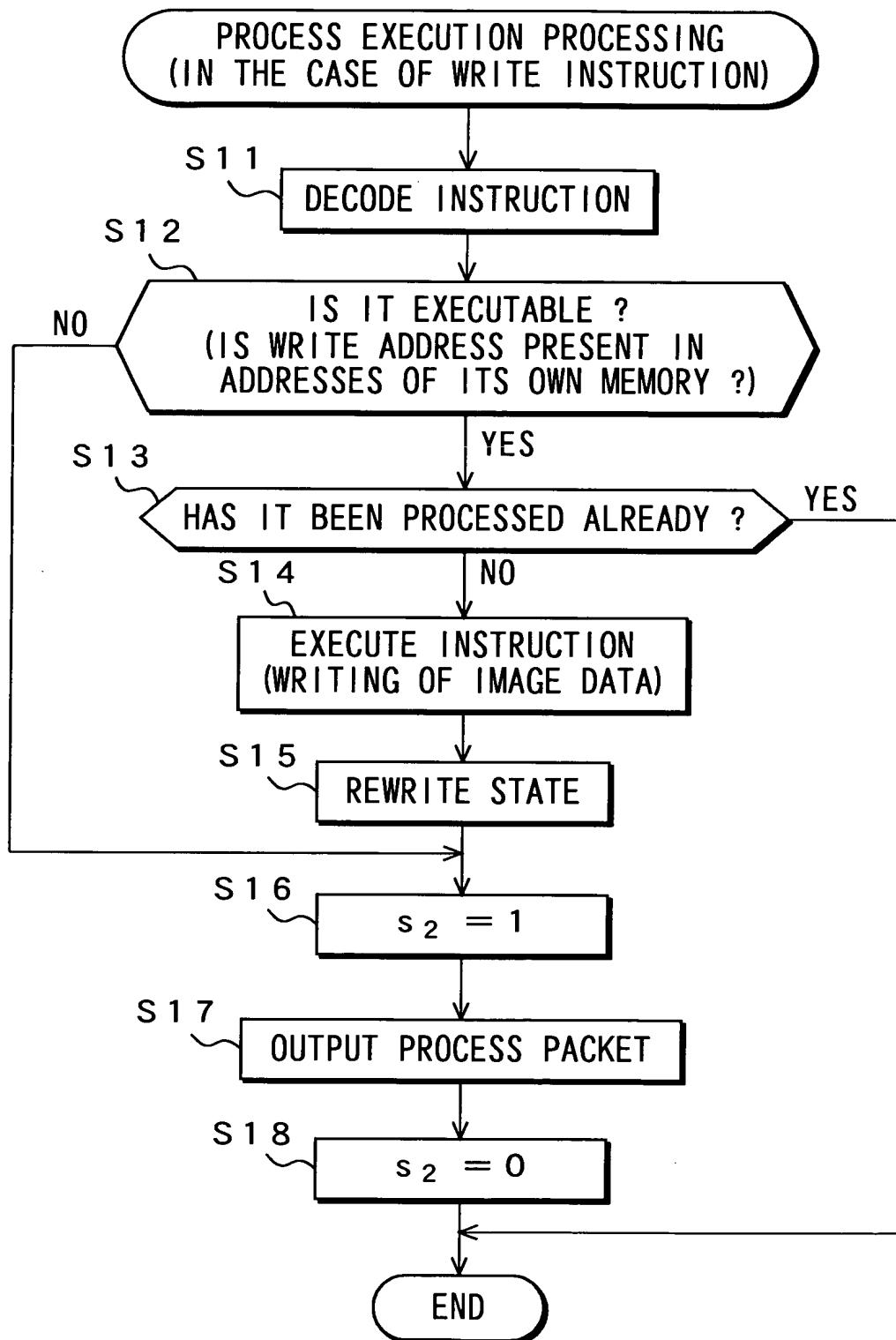


FIG. 12



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FIG. 13 A

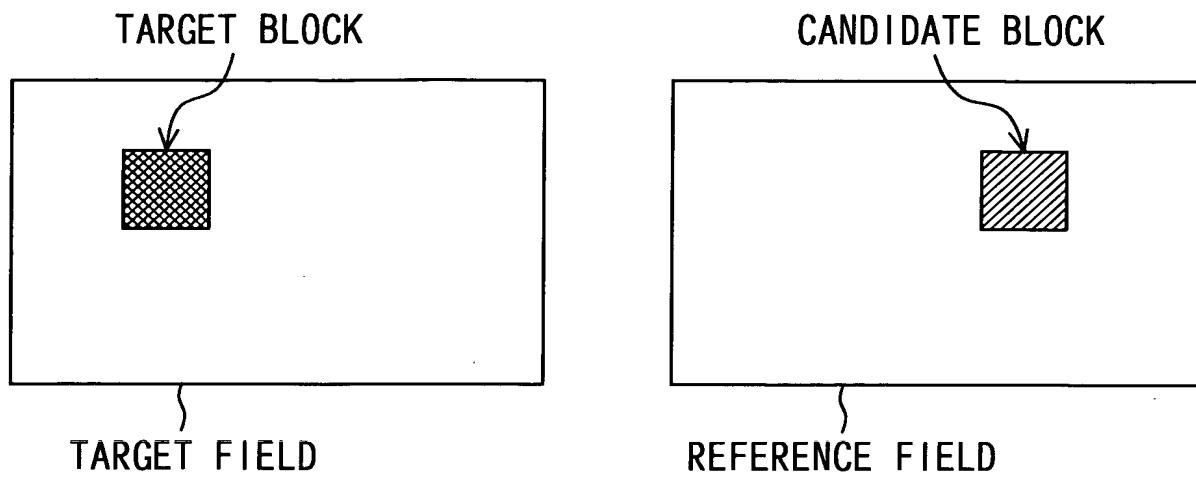
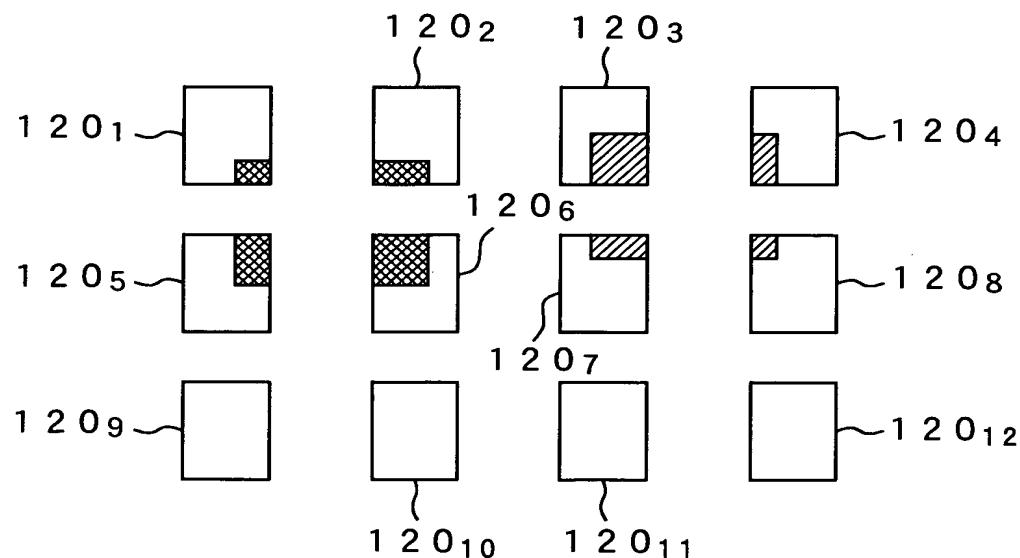


FIG. 13 B



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FIG. 14

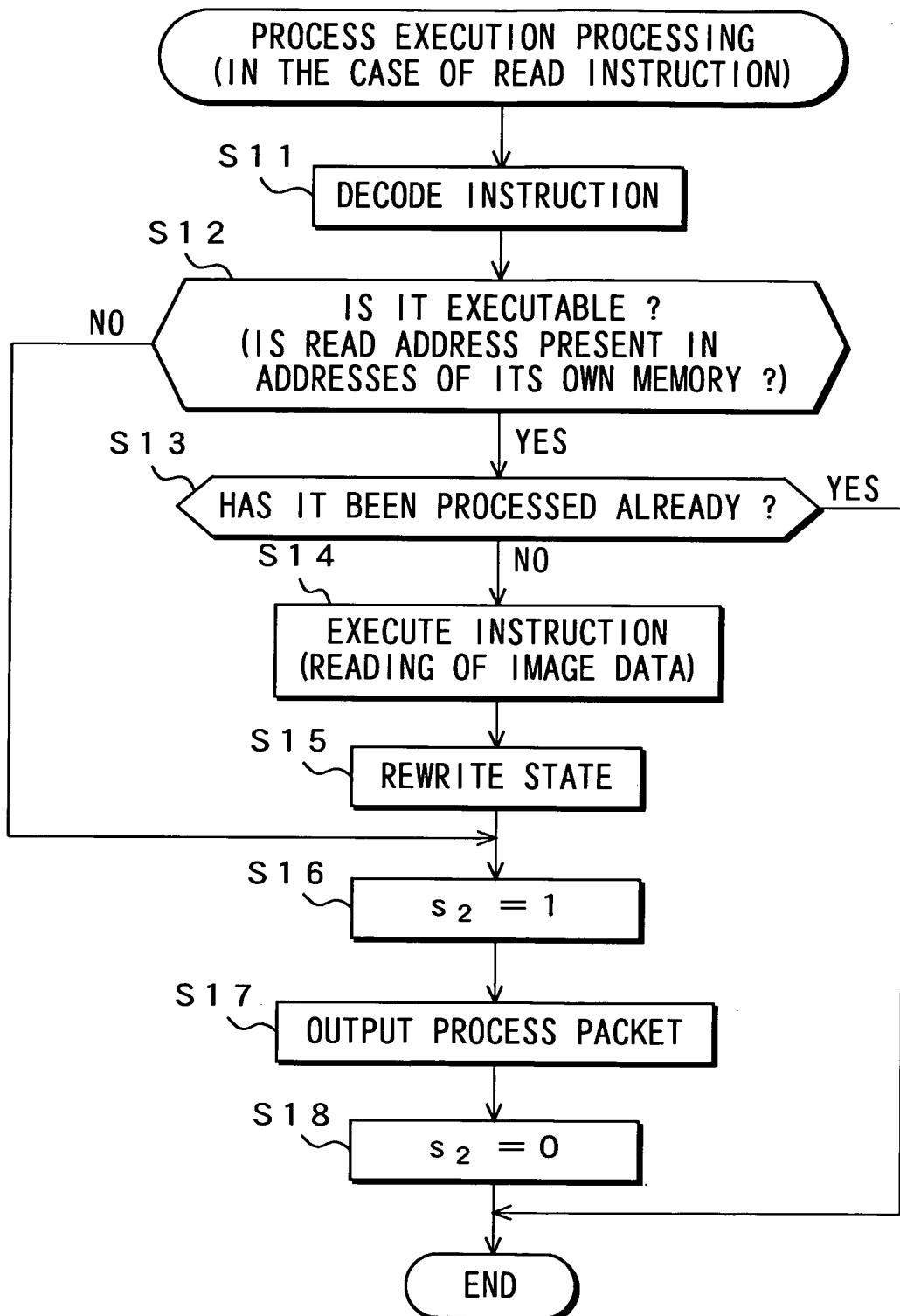


FIG. 15

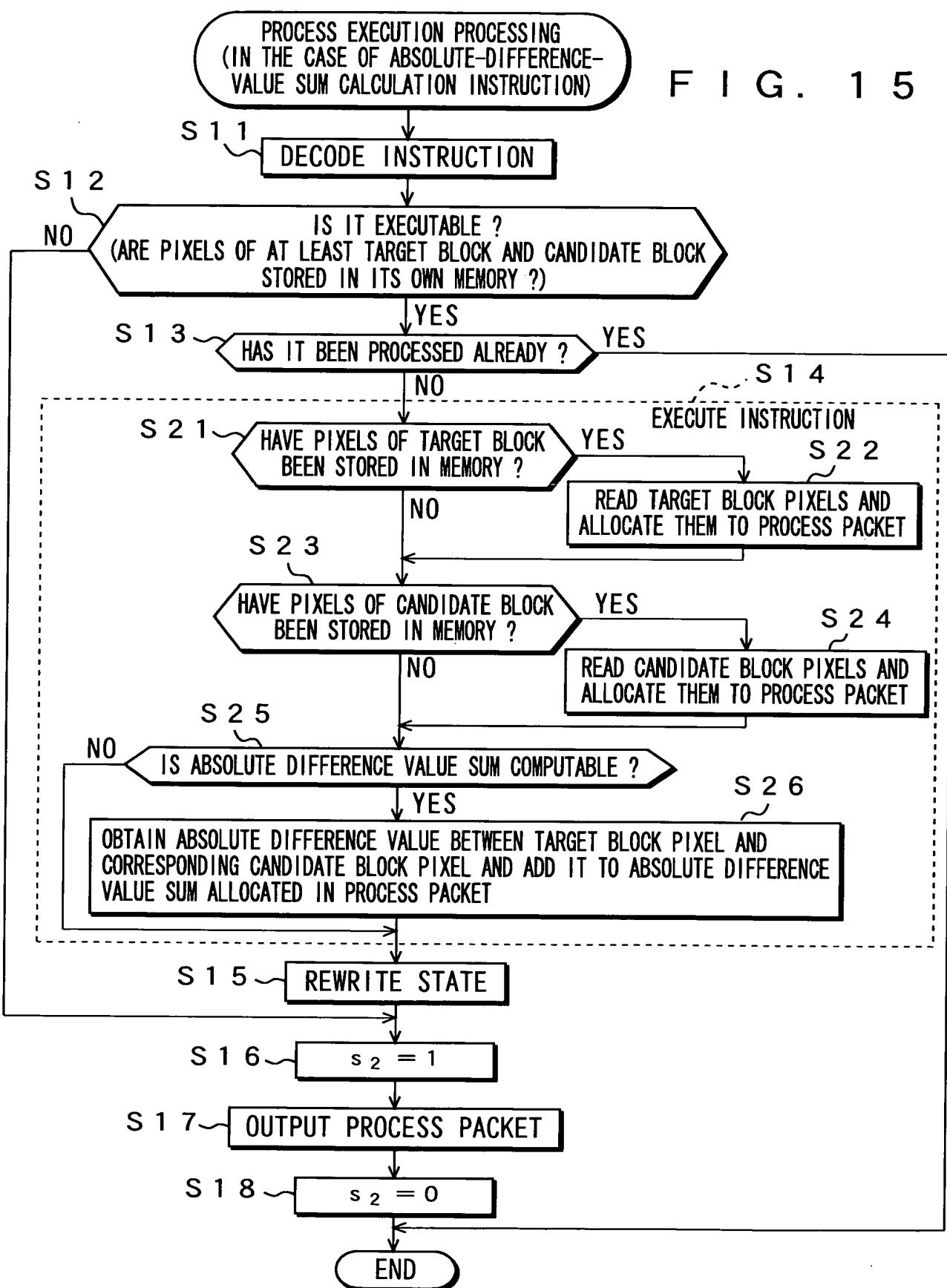
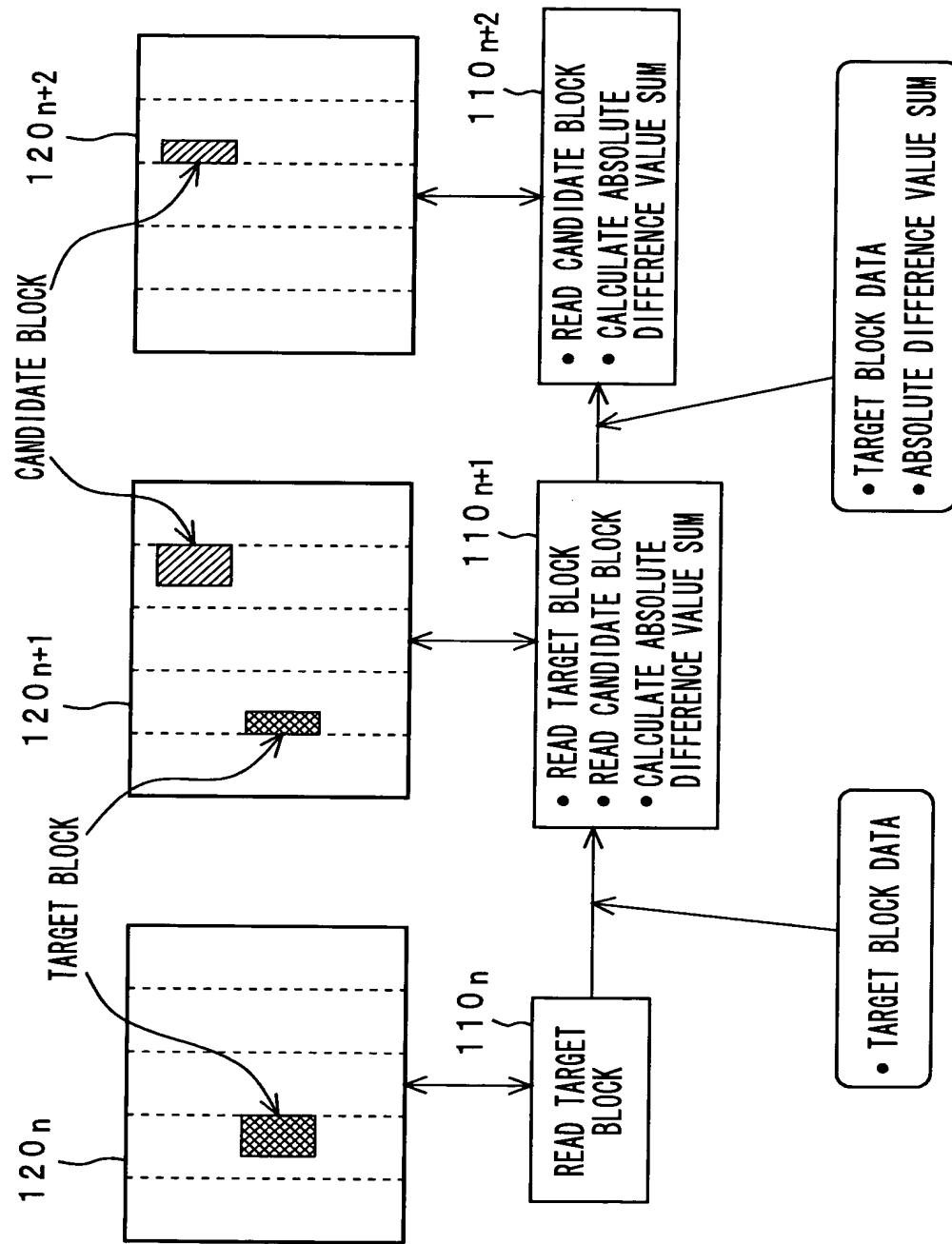


FIG. 16



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FIG. 17 A

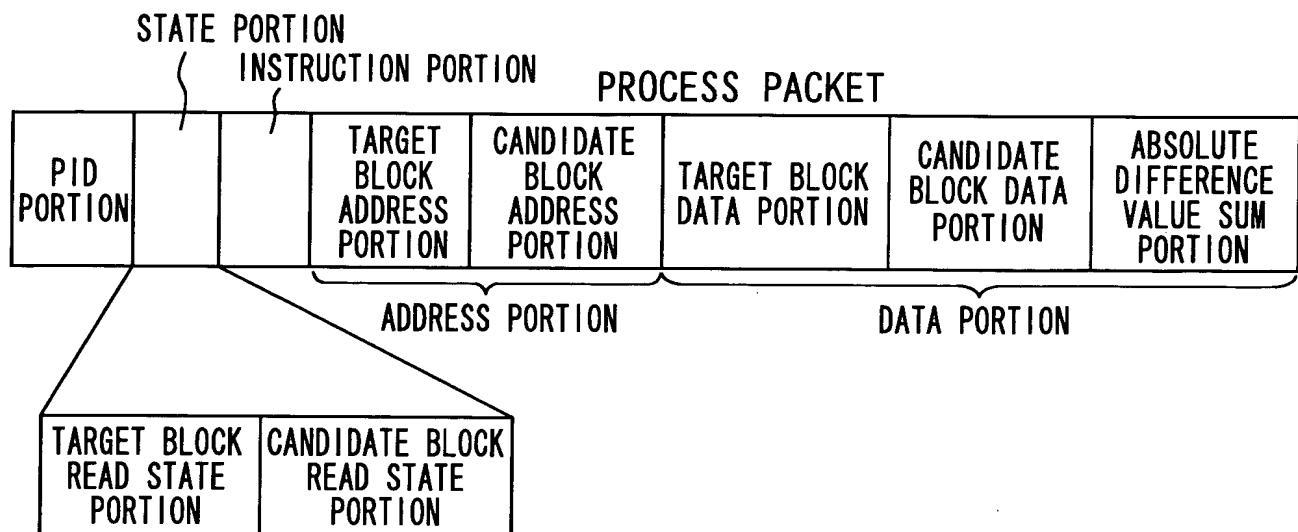


FIG. 17 B

